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August 2008

MMBFJ270 P-Channel Switch

Features

- This device is designed for low level analog switching sample and hold circuits and chopper stabilized amplifiers.
- · Sourced from process 88.



Absolute Maximum Ratings (Note1) T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V_{DG}	Drain-Gate Voltage	-30	V	
V_{GS}	Gate-Source Voltage	30	٧	
I _{GF}	Forward Gate Current	50	mA	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	°C	

Note1: These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Thermal Characteristics

Symbol	Parameter	Value	Units	
P_{D}	Total Device Dissipation Derate above 25°C	225 1.8	mW mW/°C	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note2)	556	°C/W	

Note2 : Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch

Electrical Characteristics T_C = 25°C unless otherwise noted

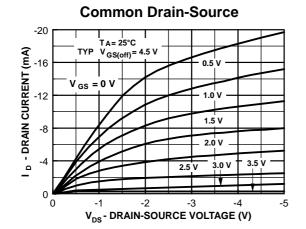
Symbol	Parameter	Test Condition	MIN	MAX	Units
Off Charac	cteristics (Note3)				
V _{(BR)GSS}	Gate-Source Breakdwon Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	30		V
I _{GSS}	Gate Reverse Current	$V_{GS} = 20V, V_{DS} = 0$		200	pA
V _{GS(off)}	Gate-Source Cutoff Voltage	$V_{DS} = -15V, I_{D} = -1.0nA$	0.5	2.0	V
On Charac	cteristics (Note3)				
I _{DSS}	Zero-Gate Voltage Drain Current *	$V_{DS} = -15V, V_{GS} = 0$	-2.0	-15	mA
gfs	Forward Transferconductance	$V_{GS} = 0V, V_{DS} = 15V, f = 1.0kHz$	6000	15000	μmhos
goss	Common- Source Output Conduc-	$V_{CC} = 0V$, $V_{DC} = 15V$, $f = 1.0kHz$		200	umhos

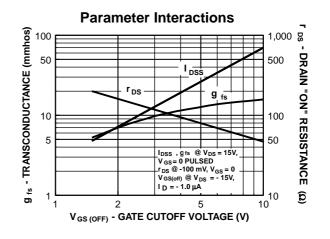
Note3 : Short duration test pulse used to minimize self-heating effect.

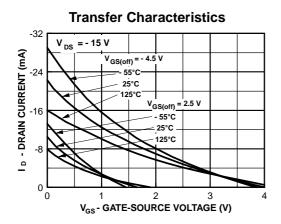
tance

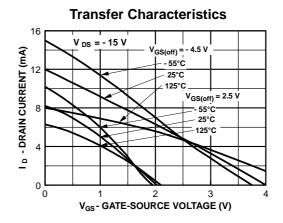
These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

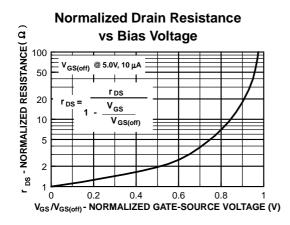
Typical Characteristics

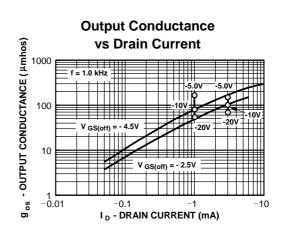




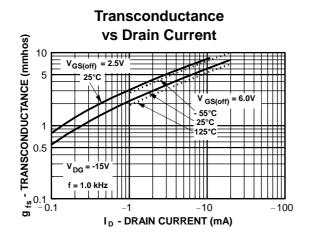


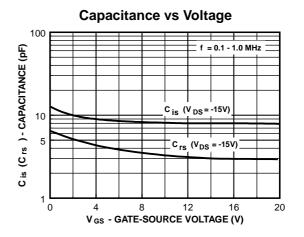


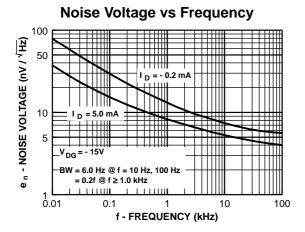


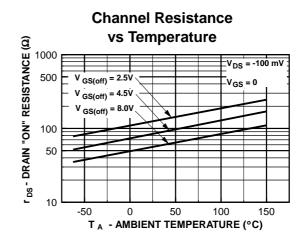


Typical Characteristics (Continued)

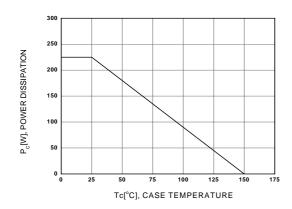






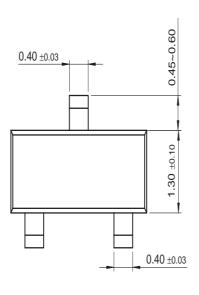


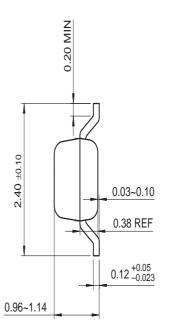
Power Derating

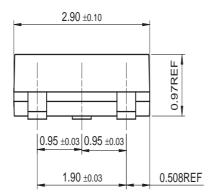


Package Dimensions

SOT-23







Dimensions in Millimeters





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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